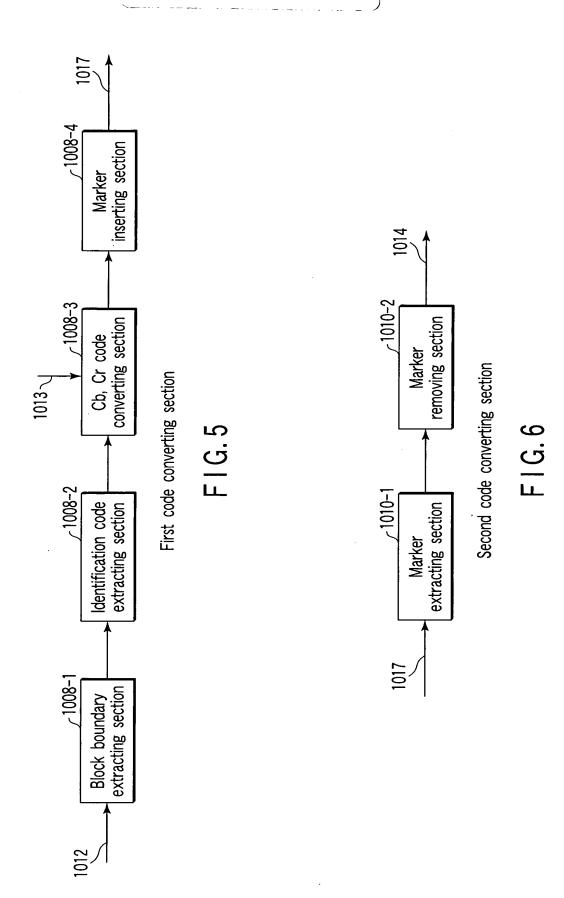


J006-15 8-9006 ~ J006-14 Code output section J006-17 -J006-16 9-9005 7-9006-7 AC Huffman table output section Code length determining AC table reference section 6-9001~ 900 J006-5 ~J006-12 Run length count section J006-1 0 determining DC Huffman table output section J006-10 DC table reference section J006-11 7 J006-3 DC component quantization differential processing result Zigzag scan section AC component —— quantization result

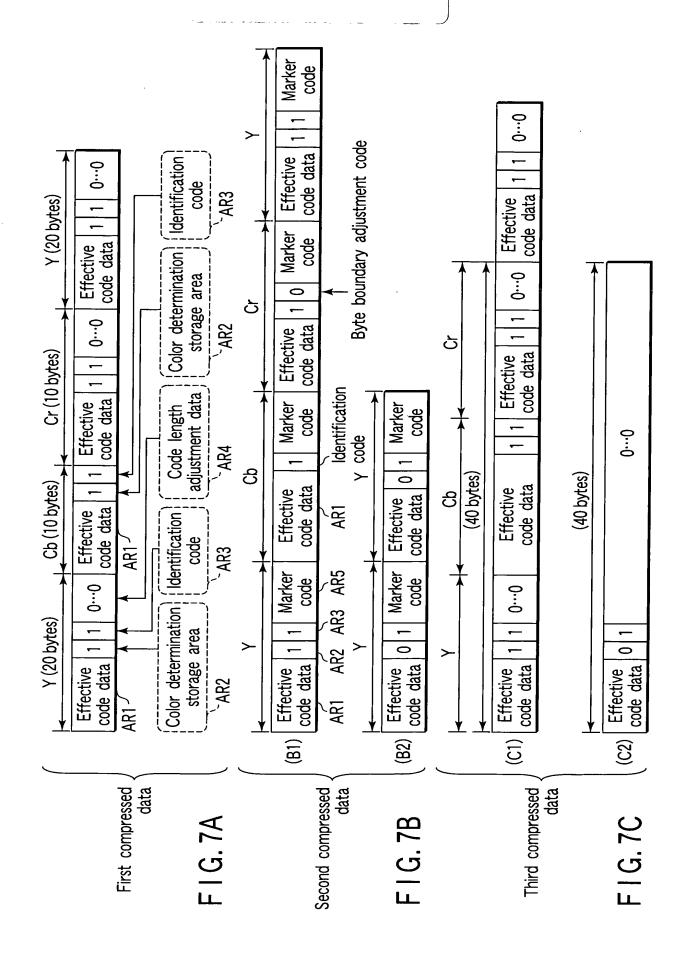
Entropy coding section J006 FIG. 3

Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 1013 Comparator Threshold "2" -1003-3Counter Comparator Threshold "1" F1G.4 ACS 1003 Adder ABS-R ABS-G ABS-B Absolute Absolute Absolute value value value SUB-R -SUB-G SUB-B Differentiator Differentiator Differentiator 1011-G R 1011-R --Ω

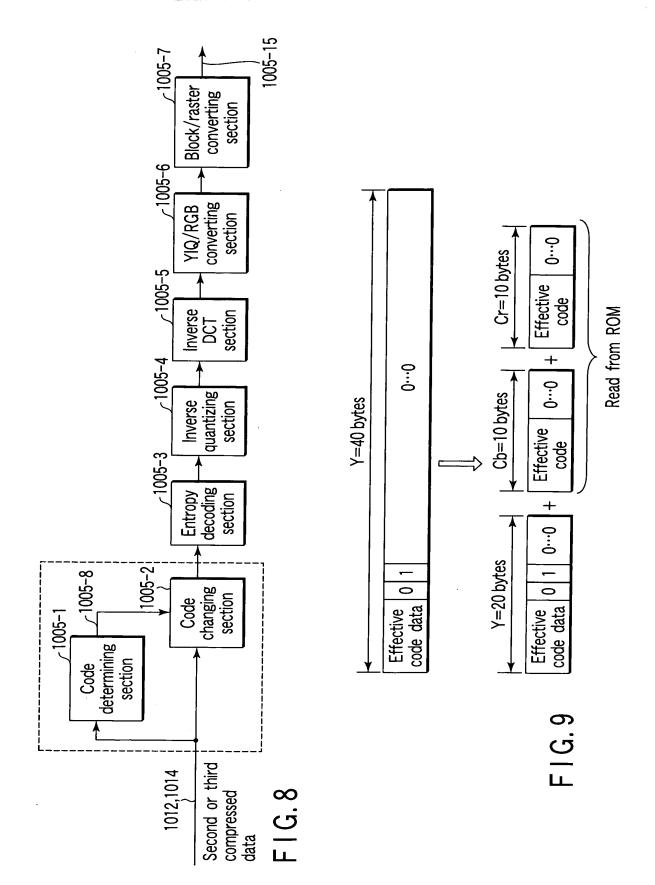
Title: APPARATUS FOR IMAGE PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



3



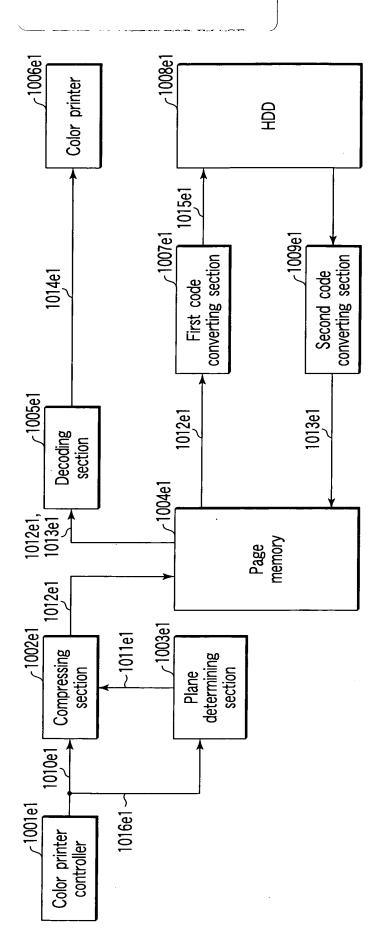
` ;



5 Block ACS ACS FIG.10 Mode signal.

	,	,	,					
Output	Block determination	0	0	0		0	-	0
	Block ACS	0	•	0	-	0		1
Input	Entire ACS	0	0	1	-	ı	ı	-
Ч	Compression mode signal 1019	ACS	ACS	ACS	ACS	Color	Color	Monochromatic

Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



F1G.12

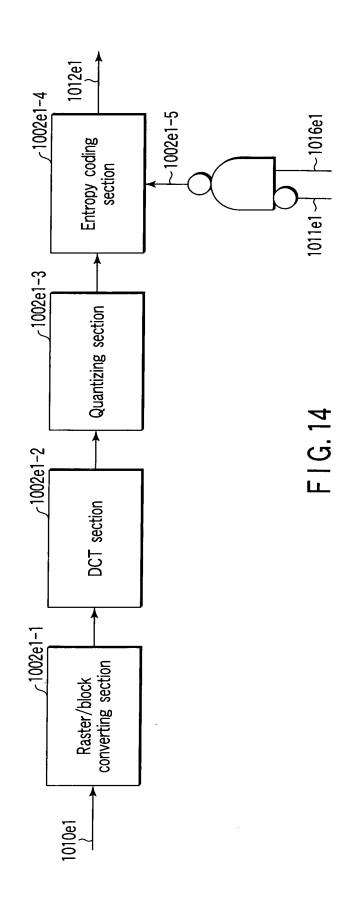
PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 → 1011e1-Y ≯1011e1-C → 1011e1-M ►1011e1-K -1003e1-10 1003e1-12 1003e1-11 1003e1-9 Comparator Comparator Comparator Comparator F1G. 13 0 0 0 0 -1003e1-5 -1003e1-6 1003e1-8 -1003e1-7 ADD ADD ADD ADD RST 71003e1-3 ~1003e1-1 1003e1-2 71003e1-4 converting section converting section converting section converting section Raster/block Raster/block Raster/block Raster/block

1010e1-M-

Title: APPARATUS FOR IMAGE

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Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454

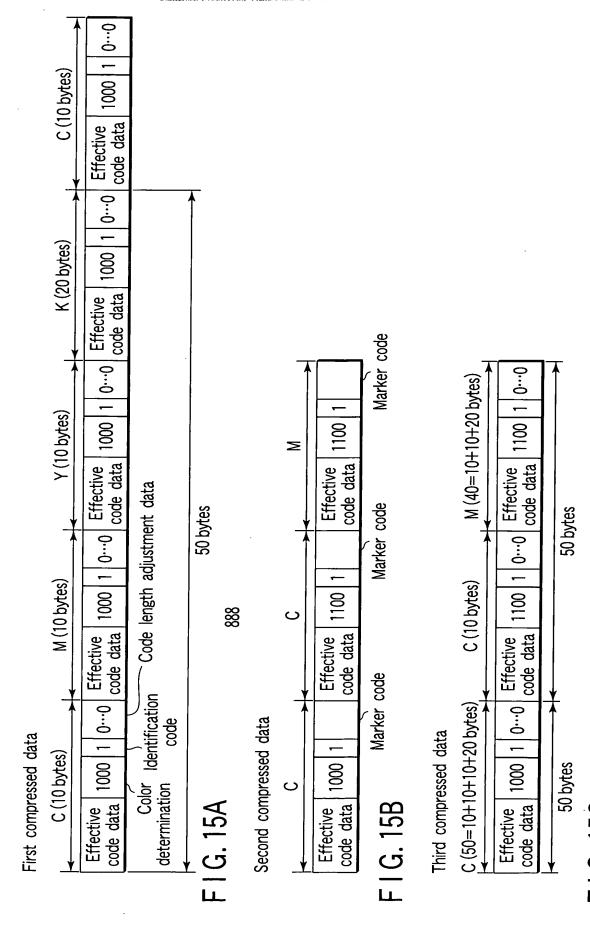
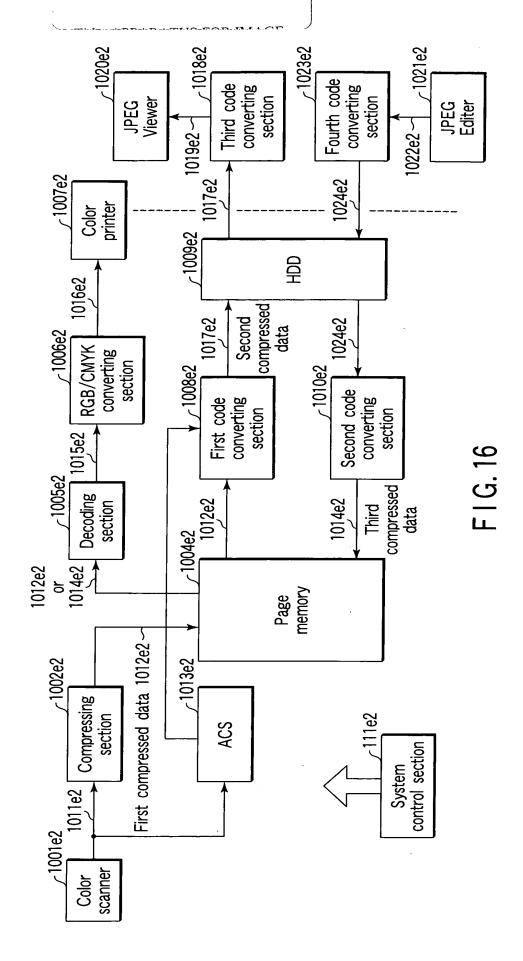
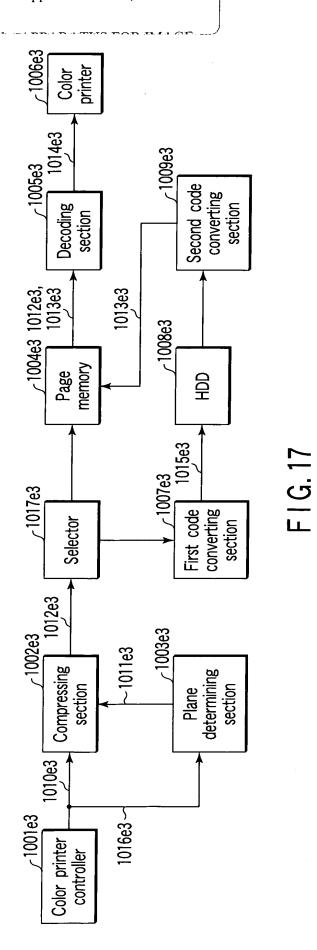
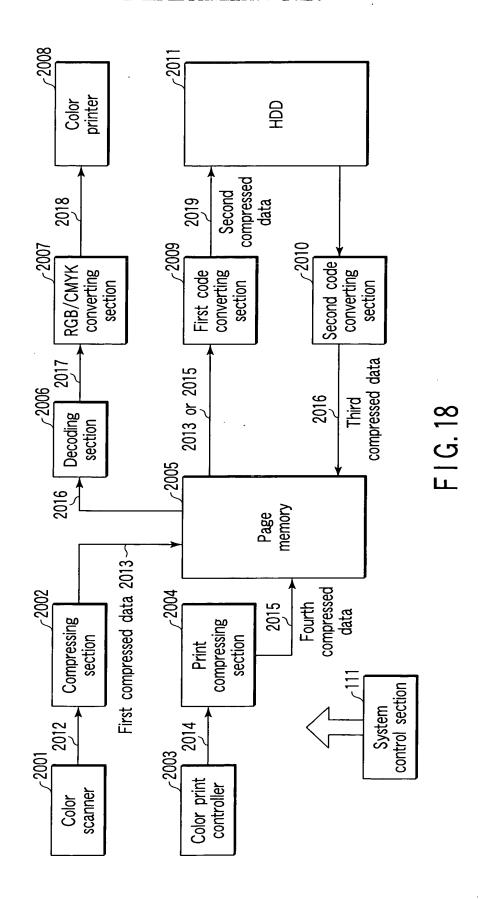


FIG. 15C

PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



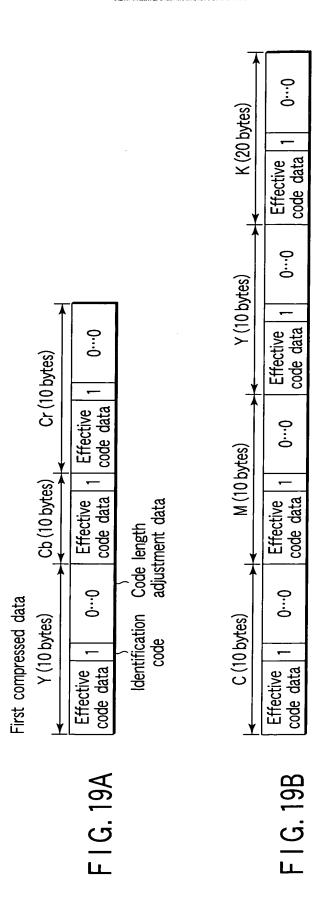


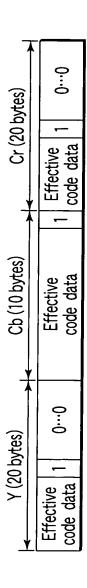


PROCESSING

Inventor(s): Sunao TABATA et al.

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454





IG. 19C

Appl. No.: 10/806,454 오 \mathfrak{A} $\overline{\mathbb{Z}}$ ට 고 50 bytes 50 bytes 9 50 bytes $\frac{2}{3}$ 50 bytes \mathfrak{S} \succeq ප **W** S Ξ ₩ 40 bytes 33 ප \Im \mathfrak{S} \overline{c} 72 0..0 \geq 0 NWC∩ 0 WCU 2 € NGC 3 MCU 1 2 $\dot{\varsigma}$ $50\times4=200$ bytes ප 00 WC ↑ 젌 오 $\overline{\mathbb{Z}}$ $\overline{\Delta}$ \mathfrak{Z} 72 Sampling for ratio of length to width of 1/2 9 ζ; \mathcal{Z} င် =9 웆 $\frac{8}{8}$ ∀ **W** ප FIG. 20D MCU ප \Im \mathbb{S} \Box \mathfrak{Z} \succeq 9 FIG. 20C FIG. 20B ∞ თ വ ~ <u></u>

PROCESSING
Inventor(s): Sunao TABATA et al.

LITIG: APPAKATUS FOR INIAOP PROCESSING

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

Y0 Y1 Cb0 Cr0 Y0 Y1 Cb0 Cr0 Y2 Y3 Cb1 Cr1

Y2 Y3 Cb1 Cr1

Print scan (data read direction)

		K 0	K1	
		λ0	۲۱	
		₩	M1	
		8	Cl	
		CO MO YO KO CO MO YO KO	CI MI YI KI CI MI YI KI	
	λ	Υ1		
	9	M		
	ප	ರ		
	2	돗		
(j)	1	Y0 Y1 Cb0 Cr0 Y0 Y1 Cb0 Cr0	Y2 Y3 Cb1 Cr1 Y2 Y3 Cb1 Cr1	
recti		1 C	3 C	
ad di		\ 0	/2 Y	
Print scan (data read direction)		Q.	ri	
(dat		0) [
can		පි	පි	
nt sc) Y1	γ3	
Pri		λ	Υ2	

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9

8 ප

오 9

S ප

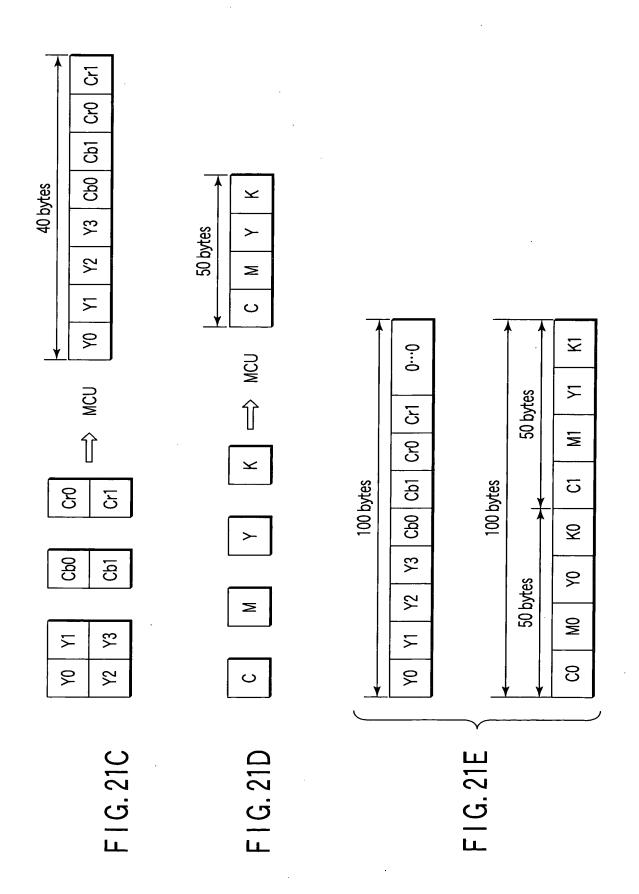
F1G.21B

F1G.21A

TITIE: APPAKA POSTOK IIVIAOL PROCESSING

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

mid ADDAD ATTIC FOR IMACE



PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 -2008e1 Color printer 异 compressed data 2019e1 2018e1 Third 72007e1 72009e1 /2010e1Second code converting RGB/CMYK First code converting converting section section section 2017e1 2013e1 or 2015e2 Fourth compressed data 72006e1 2016e1 Decoding section F1G. 22 -2005e1 2016e1 Page memory First compressed data 2013e1~ Compressing section 2014e1 control section System 2012e1

72001e1

Color scanner 72003e1

Color print controller

THE: APPAKATUS TUKTIVIAUE **PROCESSING** Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 ×64= 32 bytes 0..0 **63** 오 Y63 2 **K**63 4 bit M63 $\frac{9}{8}$ Y63 **C**63 ප 40 bytes \equiv 63 32 bytes \Box 중 F1G. 23C FIG. 23B M63 2 $\frac{9}{2}$ **C**63 8

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40 bytes

FIG. 23D

THE TAPPAKATUS FUK LVIAUL **PROCESSING** Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

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